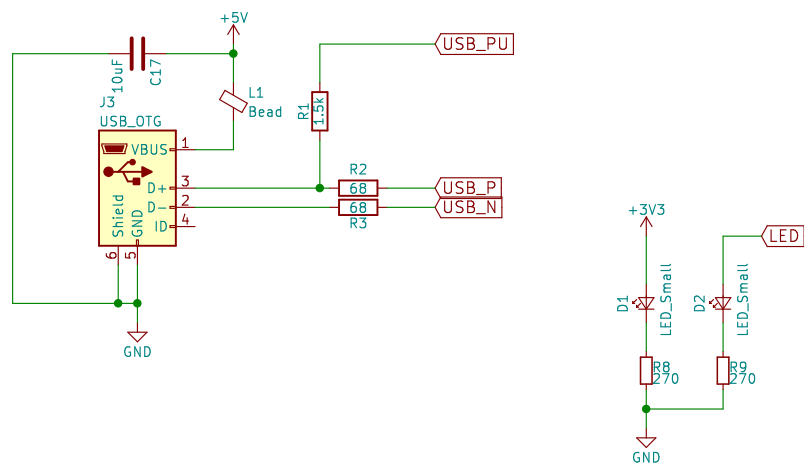
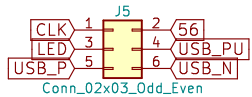
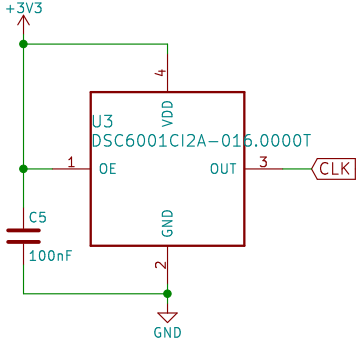


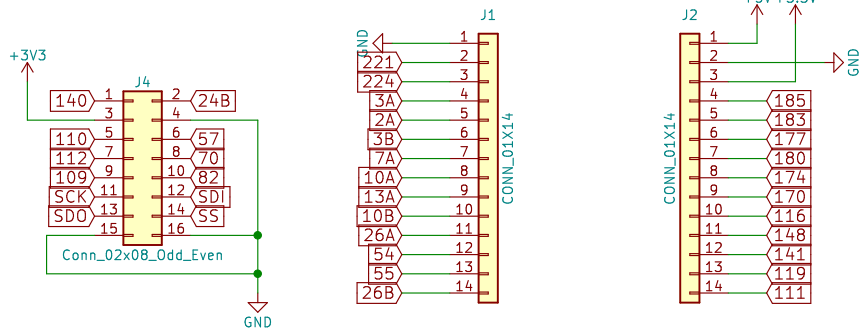
USB connection and power filtering



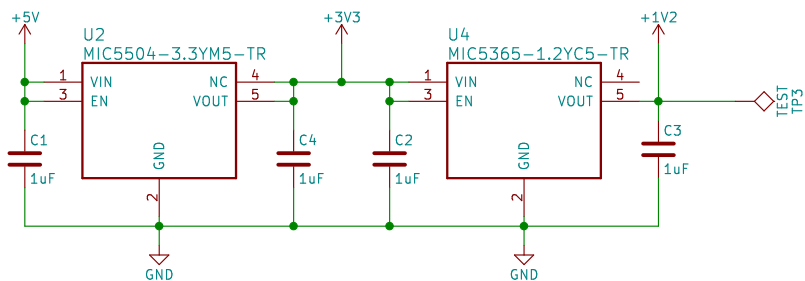
16MHz Clock



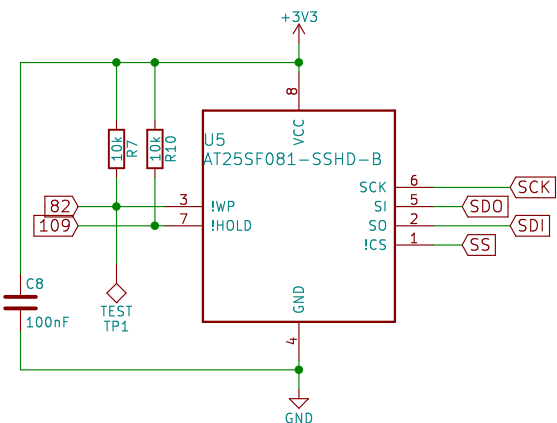
TinyFPGA Bx Pin Headers



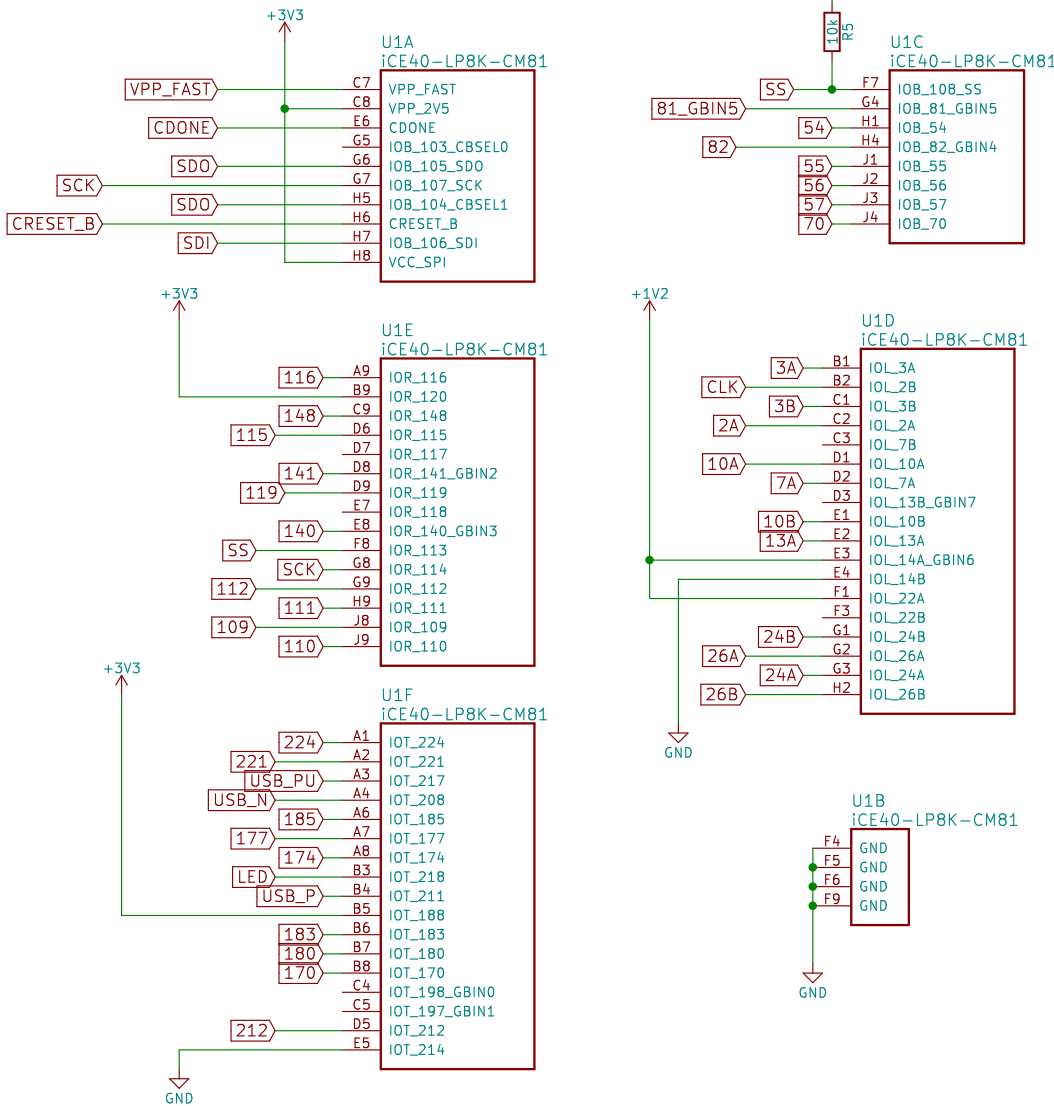
Voltage Regulation



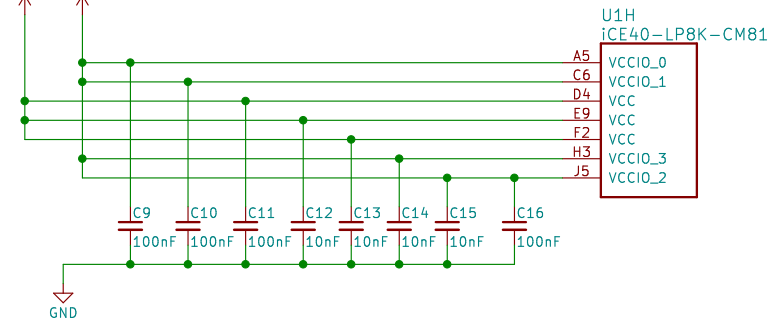
SPI flash memory for FPGA configuration



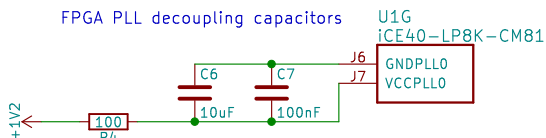
FPGA connections



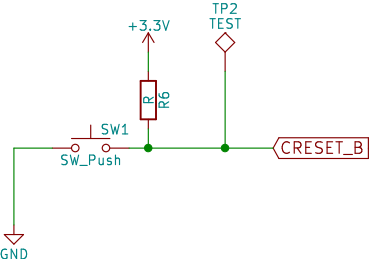
FPGA core and IO power connections and decoupling capacitors



FPGA PLL decoupling capacitors



FPGA reset button and testpoint



TinyFPGA

Sheet: /
File: TinyFPGA-BX.sch

Title: TinyFPGA Bx

Size: A3 Date:
KiCad E.D.A. kicad 4.0.7

Rev: v1.1

Id: 1/1