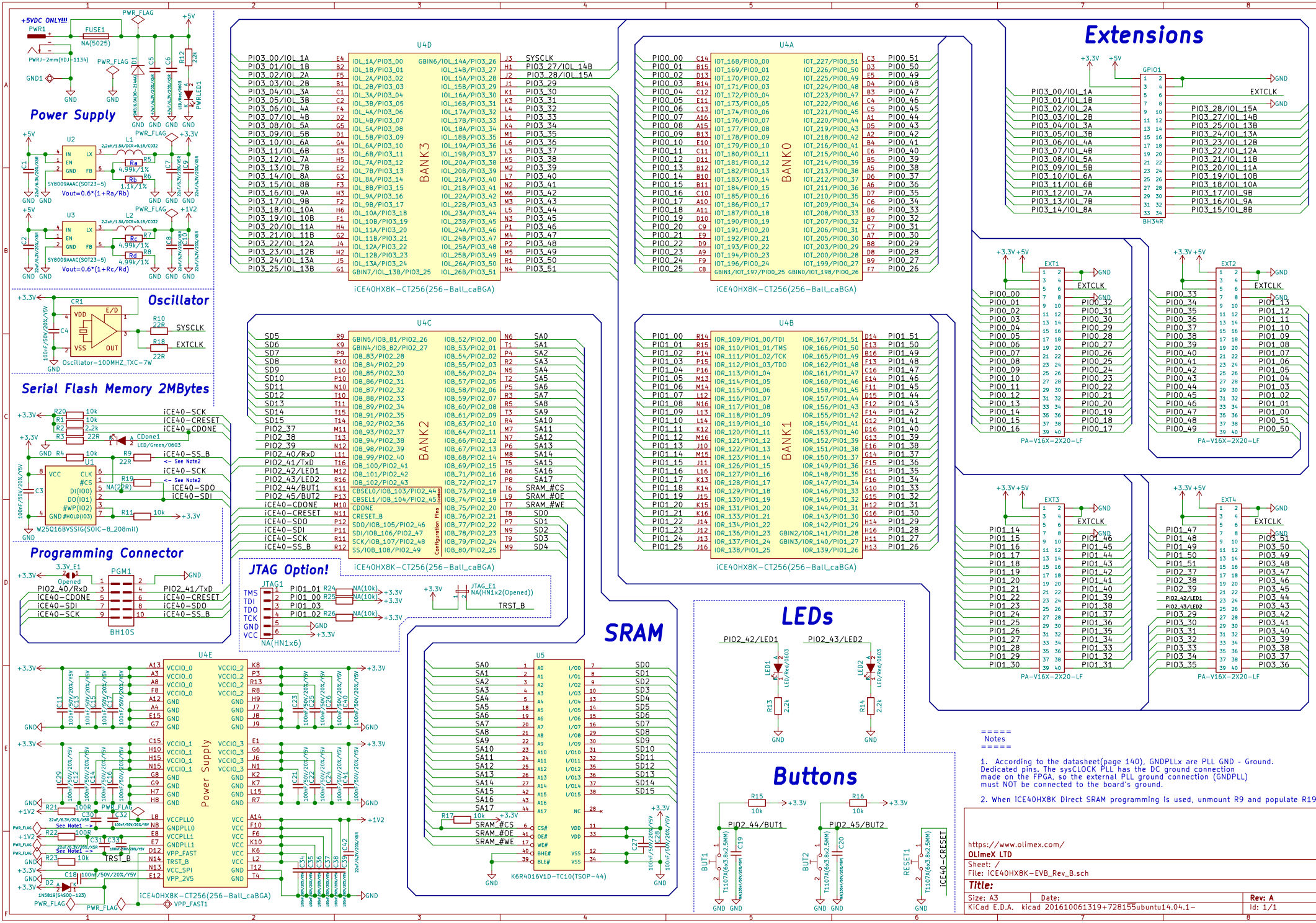
+5VDC ONLY!!!
Power Supply
Oscillator
Vout=0.6*(1+Ra/Rb)
Vout=0.6*(1+Rc/Rd)
Oscillator-100MHZ_TXC-7W
Serial Flash Memory 2MBytes
W25Q16BVSSIG(SOIC-8_208mil)
Programming Connector
BH10S
JTAG Option!
NA(HN1x6)
U4D
U4A
U4B
U4C
U4E
U5
BANK3
BANK0
BANK1
BANK2
Configuration Pins
iCE40HX8K-CT256(256-Ball_caBGA)
Power Supply
K6R4016V1D-TC10(TSOP-44)
SRAM
LEDs
Buttons
Extensions
GPIO1
BH34R
EXT1
EXT2
EXT3
EXT4
PA-V16X-2X20-LF
Notes
1. According to the datasheet(page 140), GNDPLLx are PLL GND - Ground. Dedicated pins. The sysCLOCK PLL has the DC ground connection made on the FPGA, so the external PLL ground connection (GNDPLL) must NOT be connected to the board's ground.
2. When iCE40HX8K Direct SRAM programming is used, unmount R9 and populate R19!
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Sheet: /
File: iCE40HX8K-EVB_Rev_B.sch
Title:
Size: A3
Date:
Rev: A
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